

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT(S): Sohn et al.

DOCKET: 8021-225 (SS-18860-US)

SERIAL NO.: 10/814,968

GROUP ART UNIT: 2185

FILED: March 31, 2004

EXAMINER: Campos, Yaima

**FOR: INTEGRATED CIRCUIT HAVING A MEMORY CELL ARRAY  
CAPABLE OF SIMULTANEOUSLY PERFORMING A DATA READ  
OPERATION AND A DATA WRITE OPERATION**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT UNDER 37 C.F.R. § 1.111**

Sir:

In response to the Office Action dated October 18, 2006, please consider  
the following amendments and remarks.